

REMARKS

Claims 1, 3-4, 6-12, 14 and 16-26 are pending in this application. By this Amendment, claims 1, 6-12, 14, 16, 22, 24 and 26 are amended and claim 2 is canceled without prejudice or disclaimer. Various amendments are made for clarity and are unrelated to issues of patentability.

Entry of the amendments is proper under 37 C.F.R. §1.116 because the amendments: (1) place the application in condition for allowance; (2) do not raise any new issues requiring further search and/or consideration; and/or (3) place the application in better form for appeal, should an appeal be necessary. More specifically, the above amendments merely clarify the previously-claimed relationship between the identifiers and the storage areas. Thus, no new issues are raised. Entry is proper under 37 C.F.R. §1.116.

The Office Action rejects claims 1-28 on the ground of nonstatutory obviousness-type double patenting over claims 1-23 of U.S. Patent Publication 2002/0080788. This publication corresponds to U.S. Patent Application No. 10/020,872, filed December 19, 2001, which is currently pending. Applicant is attaching a Terminal Disclaimer to obviate the provisional obviousness-type double patenting rejection. Withdrawal of the rejection is respectfully requested

The Office Action rejects claims 1-4, 6-12, 14 and 16-26 under 35 U.S.C. §103(a) over U.S. Patent 6,594,267 to Dempo. The rejection is respectfully traversed with respect to the pending claims.

Independent claim 1 recites sequentially storing the divided CPS packets into first storage areas in accordance with corresponding virtual paths/virtual channels (VPs/VCs) of the

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respective CPS packets, and sequentially storing first identifiers of the first storage areas, each first identifier corresponding to a different one of the first storage areas. Independent claim 1 also recites reading the stored CPS packets in the order of the stored first identifiers, sequentially storing the read CPS packets in second storage areas used to route the CPS packets to each destination in accordance with respective destination channel identifiers (CIDs), and sequentially storing second identifiers of the second storage areas, each second identifier corresponding to a different one of the second storage areas. Still further, independent claim 1 recites reading the CPS packets, in the order of the second identifiers, from the second storage areas and multiplexing the read CPS packets to generate a reconstructed AAL2 cell.

Dempo does not teach or suggest at least these features of independent claim 1. More specifically, when discussing features relating to sequentially storing first identifiers of the first storage unit, the Office Action (on page 7) states that addresses concerning each of the CPS-PDUs are stored in a FIFO memory and addresses #1 concerning each of the CPS-PDUs are stored in FIFO memory 13 where each address #1 corresponds to VPI & VCI. However, these features do not correspond to the previously claimed subject matter or the newly claimed subject matter. More specifically, these alleged features of Dempo do not relate to sequentially storing first identifiers of the first storage areas, wherein each first identifier corresponding to a different one of the first storage areas. Dempo does not teach or suggest sequentially storing CPS packets in first storage areas and sequentially storing first identifiers of the first storage areas as recited in independent claim 1. The addresses concerning each of the CPS-PDUs being stored in a FIFO memory 13 does not correspond to these claimed features.

Additionally, Dempo does not teach or suggest sequentially storing read CPS packets in second storage areas and sequentially storing second identifiers of the second storage areas, wherein each second identifier corresponding to a different one of the second storage areas. More specifically, when discussing the previously claimed features, the Office Action (on page 7) states that the FIFO memory 18 stores CPS packets output by the processing section 17, addresses concerning each of the CPS packets are stored in FIFO memory 19, a FIFO memory 18 stores CPS packets outputted by the processing section 17 and addresses #2 concerning each of the CPS packets are stored in FIFO memory 19 where each address # 2 corresponds to an output CID. However, these features clearly do not relate to storing the packets in second storage areas and sequentially storing second identifiers of the second storage areas, where each second identifier corresponding to a different one of the second storage areas.

Still further, Dempo does not teach or suggest reading the stored CPS packets in the order of the stored first identifiers. Additionally, Dempo does not teach or suggest reading the CPS packets, in order of the second identifiers, from the second storage areas. Dempo does not disclose features relating to the order of the first/second identifiers.

For at least the reasons set forth above, Dempo does not teach or suggest all the features of independent claim 1. Thus, independent claim 1 defines patentable subject matter.

Independent claim 14 recites a first memory that sequentially stores the divided CPS packets into first storage areas and sequentially stores first identifiers of the first storage areas, each first identifier corresponding to a different one of the first storage areas. Independent claim 14 also recites a CPS packet switching unit that reads the stored CPS packets from the first

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storage areas in an order of the stored first identifiers. Still further, independent claim 14 recites a second memory that sequentially stores the routed CPS packets into second storage areas and sequentially stores second identifiers of the second storage areas, each second identifier corresponding to a different one of the second storage areas. Independent claim 14 also recites an assembly processing unit that reads the CPS packets from the second storage areas in an order of the second identifiers.

For at least similar reasons as set forth above, Dempo does not teach or suggest at least these features of independent claim 14. Thus, independent claim 14 defines patentable subject matter.

Independent claim 16 recites first, second, third, and fourth memories, wherein each memory has a plurality of storage areas. Independent claim 16 also recites a reassembly processing unit that divides an input AAL2 cell into the AAL2 type CPS packets, stores the divided CPS packets in different first storage areas of the first memory in accordance with corresponding virtual paths/virtual channels (VPs/VCs), and stores first identifiers of the different first storage areas in the second memory, each first storage area having a different first identifier. Still further, independent claim 16 also recites a CPS packet switching unit that reads the CPS packets stored in the first memory in an order of the first identifiers stored in the second memory, stores the read CPS packets in different second storage areas of the third memory in accordance with corresponding destination channel identifiers (CIDs), and stores second identifiers of the second storage areas in the fourth memory, each second storage area having a different second identifier. Independent claim 16 also recites an assembly processing

unit that reads the CPS packets stored in the third memory in an order of the second identifiers stored in the fourth memory.

For at least similar reasons as set forth above, Dempo does not teach or suggest all the features of independent claim 16. Thus, independent claim 16 defines patentable subject matter.

Accordingly, each of independent claims 1, 14 and 16 defines patentable subject matter. Each of the dependent claims depends from one of the independent claims and therefore defines patentable subject matter at least for this reason. In addition, the dependent claims recite features that further and independently distinguish over the applied references.

For example, dependent claim 6 recites generating a first reference table that maps each of the first identifiers with the corresponding virtual path/virtual channel (VP/VC), and generating a second reference table that maps each of the second identifiers with the corresponding channel identifier (CID). See also dependent claim 17. The Office Action cites Dempo's col. 18, lines 55-58 as corresponding to a path setting table that maps addresses #1 with input VPIs and VCIs and a path setting table that maps addresses #2 with output CIDs. However, these features clearly do not relate to the claimed first and second identifiers as recited in dependent claim 6 (and similarly dependent claim 17). Thus, dependent claims 6 and 17 define patentable subject matter at least for this additional reason.

CONCLUSION

In view of the foregoing, it is respectfully submitted that the application is in condition for allowance. Favorable consideration and prompt allowance of claims 1, 3-4, 6-12, 14 and 16-26 are earnestly solicited. If the Examiner believes that any additional changes would place the

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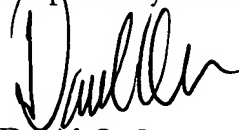
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application in better condition for allowance, the Examiner is invited to contact the undersigned attorney at the telephone number listed below.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,



David C. Oren

Registration No. 38,694

Attachment: Terminal Disclaimer

P.O. Box 221200

Chantilly, Virginia 20153-1200

(703) 766-3777 DCO/kah

Date: April 16, 2007

Please direct all correspondence to Customer Number 34610